IR-1641 (2-1929)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent application of:

Thomas Herman

Date: June 8, 2004

Serial No.: 10/044,427

Group Art Unit: 2822

Filed: November 9, 2001

Examiner: Monica Lewis

Notice of Allowance: March 29, 2004

Confirmation No.: 1934

For:

MOSFET WITH REDUCED THRESHOLD VOLTAGE AND ON

RESISTANCE AND PROCESS FOR ITS MANUFACTURE

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUBMISSION OF SUPPLEMENTAL DECLARATION

Sir:

In the Office Action dated September 27, 2002, the Examiner required a new oath or declaration in compliance with 37 C.F.R. 1.67(a), because the originally submitted declaration was found to be defective. Accordingly, Applicants now submit a supplemental declaration executed by Jianjun Cao showing his correct address and citizenship to comply with the requirement.

Entry is respectfully requested.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 8, 2004:

Brendan J. Kennedy
Name of applicant, assignee or
Registered Representative

June 8, 2004

Date of Signature

Respectfully submitted,

Brendan J. Kennedy

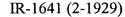
Registration No.: 41,890

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Attn: Official Draftsperson

SUBMISSION OF FORMAL DRAWINGS

Sir:

Enclosed herewith please find two (2) sheets of formal drawings containing Figures 1-5 for the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, Attn: Official Draftsperson, on June 8, 2004:

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